



Eures Trento – Agenzia del Lavoro Provincia Autonoma di Trento

<b>PROFILE</b>	<b>RESEARCHER IN ADVANCED CMOS IMAGE SENSORS AND INTEGRATED CIRCUITS DESIGN</b>
<b>N. POSTS</b>	1
<b>LOCATION</b>	TRENTO (ITALY)
<b>VALID TILL</b>	10.12.2020
<b>DESCRIPTION</b>	<p>The successful candidate will work primarily on the development of highly innovative CMOS sensors targeting specialty image sensors with single-photon detection and embedded processing capabilities. The work will be mainly performed in the context of the European project FastGhost, targeting at realizing high-resolution and real-time quantum ghost imaging. The candidate, although with a strong focus on the mixed-signal integrated circuit design, is expected to participate to the entire development flow in a cooperative and structured team, starting from the modeling of the detector to the definition of the sensor architecture, from the design and simulations of the circuit schematics to the chip layout, and finally taking care of the electrical and functional characterization of the fabricated devices up to the module level. The activities will be carried out in collaboration with other researchers of the team, and it will be requested to actively participate to meetings with project partners. Finally, the candidate is requested to actively contribute to the scientific dissemination of the group.</p> <p><b>Requirements:</b></p> <ul style="list-style-type: none"><li>Master degree in Electronics or Physics;</li><li>Strong background on microelectronics and IC design (focus on analog design flow);</li><li>Good skills on IC design tools (Cadence and/or Mentor Graphics);</li><li>Good scientific background and ability to write scientific papers;</li><li>Excellent knowledge of English (oral and written);</li><li>Effective teamwork attitude;</li><li>Capability of independently carrying out assigned tasks;</li><li>Enthusiastic attitude to research activity.</li></ul> <p><b>Preferred:</b></p> <ul style="list-style-type: none"><li>PhD in Electronics, Physics or related fields or equivalent 3yrs research experience;</li><li>Experience on image sensors design or sensors readout design;</li><li>Experience in device physics/simulation;</li><li>Experience in IC digital design, VHDL, FPGA;</li><li>Good skills on LabView and/or Matlab, C++;</li><li>Scientific publications in the field of image sensors or sensor readout design;</li><li>Visiting scholarship or experience at international level.</li></ul> <p><b>Employment:</b> fixed term contract (2 years) full time; <u>Gross annual salary:</u> from about € 34.500 to € 39.500, depending on the background of the candidate More info about benefits at <a href="https://www.welfarefbk.info/">https://www.welfarefbk.info/</a></p>
<b>HOW TO APPLY</b>	For more info and on line application see at <a href="https://jobs.fbk.eu/">https://jobs.fbk.eu/</a>